

### Silicon Variable Capacitance Diodes

- For FM radio tuners with extended frequency band
- High tuning ratio at low supply voltage (car radio)
- Monolithic chip (common cathode) for perfect dual diode tracking
- Coded capacitance groups and group matching available
- Pb-free (RoHS compliant) package<sup>1)</sup>
- Qualified according AEC Q101



#### BB814



Туре	Package	Configuration	<b>L<sub>S</sub></b> (nH)	Marking
BB814	SOT23	common cathode	1.8	SH1/2*

\*For differences see next page Capacitance groups

## **Maximum Ratings** at $T_A = 25^{\circ}$ C, unless otherwise specified

Parameter	Symbol	Value	Unit
Diode reverse voltage	V <sub>R</sub>	18	V
Peak reverse voltage-	V <sub>RM</sub>	20	
Forward current	/ <sub>F</sub>	50	mA
Operating temperature range		-55 125	°C
Storage temperature	T <sub>sta</sub>	-55 150	

<sup>1</sup>Pb-containing package may be available upon special request





Parameter	Symbol	Values		Unit	
		min.	typ.	max.	
DC Characteristics					
Reverse current	I <sub>R</sub>				nA
V <sub>R</sub> = 16 V		-	-	20	
$V_{\rm R} = 16 \text{ V}, \ T_{\rm A} = 60 \text{ °C}$		-	-	200	
AC Characteristics					
Diode capacitance <sup>1)</sup>	CT				pF
$V_{R} = 2 V, f = 1 MHz$		43	44.75	46.5	
$V_{\rm R} = 8 \text{ V}, f = 1 \text{ MHz}$		19.1	20.8	22.7	
Capacitance ratio	C <sub>T2</sub> /C <sub>T8</sub>	2.05	2.15	2.25	
$V_{\rm R} = 2 \text{ V}, V_{\rm R} = 8 \text{ V}, f = 1 \text{ MHz}$					
Capacitance matching <sup>2)</sup>	$\Delta C_{\rm T}/C_{\rm T}$	-	-	3	%
$V_{\rm R} = 2 \text{ V}, V_{\rm R} = 8 \text{ V}, f = 1 \text{ MHz}$					
Series resistance	r <sub>S</sub>	-	0.18	-	Ω
$V_{\rm R} = 2 \text{ V}, f = 100 \text{ MHz}$					
Q factor	Q	-	200	-	
$f = 100 \text{ MHz}, V_{\text{R}} = 2 \text{ V}$					

# **Electrical Characteristics** at $T_A = 25^{\circ}$ C, unless otherwise specified

<sup>1</sup>Capacitance groups at 2V and 8V, coded 1; 2  $C_{T}/groups$  1 2

CI/gr	Jups	I	2
C <sub>2V</sub>	min	43pF	44.5pF

C<sub>2V</sub> max 45pF 46.5pF

C<sub>8V</sub> min 19.1pF 19.75pF

C<sub>8V</sub> max 21.95pF 22.7pF

<sup>2</sup>For details please refer to Application Note 047.



**Diode capacitance**  $C_{T} = f(V_{R})$ 

f = 1 MHz



**Capacitance ratio**  $C_{\text{Tref}}/C_{\text{T}} = f (V_{\text{R}})$ 

f = 1 MHz





### Package Outline 0.15 MIN. **1**±0.1 0.1 MAX. 2.9 ±0.1 B 由3 2.4 ±0.15 2 0.4 +0.1 1) 0.08...0.15 C 0.95 0...8° 1.9 ⊕0.25 M B C = 0.2 M A 1) Lead width can be 0.6 max. in dambar area Foot Print 0.8 က 0.9 0.8 Marking Layout (Example) (infineon Manufacturer 9 2005, June S Date code (YM) BCW66 Pin 1 Type code Standard Packing Reel ø180 mm = 3.000 Pieces/Reel Reel ø330 mm = 10.000 Pieces/Reel 0.2 0.9 2.65

1.15

3.15

Pin 1



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